



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

In re the Application of

Koji SUZUKI

Application No.: 09/970,763

Group Art Unit: 2812

Filed: October 5, 2001

Docket No.: 110596

For: APPARATUS AND METHODS FOR PROCESSING SURFACE OF SEMICONDUCTOR  
SUBSTRATE

INFORMATION DISCLOSURE STATEMENT

Director of the U.S. Patent and Trademark Office  
Washington, D.C. 20231

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- 2. Relevance of the non-English language references A and B are discussed in the present specification.

3. English-language Abstracts of the non-English language references A-D, and an English-language translation of Reference E, are attached hereto.

Respectfully submitted,



James A. Oliff  
Registration No. 27,075

Edward A. Brown  
Registration No. 35,033

JAO:EAB/ldg

Date: November 28, 2001

**OLIFF & BERRIDGE, PLC**  
**P.O. Box 19928**  
**Alexandria, Virginia 22320**  
**Telephone: (703) 836-6400**

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